(54) Title of the invention : A PROTECTIVE HEADGEAR FOR A VISUALLY IMPAIRED USER

(51) International classification : G09B0021000000, G10L0013000000, A61B0005110000, G01S0013880000, G08G0001160000	**(71)Name of Applicant :**
	  1)Chitkara University
	    Address of Applicant :Chitkara University, Chandigarh-Patiala National Highway, Village Jhansla, Rajpura, Punjab - 140401, India. Patiala ----------- -----------
	  2)Chitkara Innovation Incubator Foundation
	Name of Applicant : NA
	Address of Applicant : NA
	(72)**Name of Inventor :**
	  1)MALHOTRA, Reetu
	Address of Applicant :Professor, Applied Sciences, CUIET, Chitkara University, Chandigarh-Patiala National Highway, Village Jhansla, Rajpura, Punjab - 140401, India. Patiala ----------- -----------
	  2)SHARMA, Shail
	Address of Applicant :Computer Science and Engineering, CUIET, Chitkara University, Chandigarh-Patiala National Highway, Village Jhansla, Rajpura, Punjab - 140401, India. Patiala ----------- -----------
	  3)VIRMANI, Vrinda
	Address of Applicant :Computer Science and Engineering, CUIET, Chitkara University, Chandigarh-Patiala National Highway, Village Jhansla, Rajpura, Punjab - 140401, India. Patiala ----------- -----------
	  4)GUPTA, Pratham
	Address of Applicant :Computer Science and Engineering, CUIET, Chitkara University, Chandigarh-Patiala National Highway, Village Jhansla, Rajpura, Punjab - 140401, India. Patiala ----------- -----------

(86) International Application No : NA Filing Date : NA	
(87) International Publication No : NA	
(61) Patent of Addition to Application Number : NA Filing Date : NA	
(62) Divisional to Application Number : NA Filing Date : NA	

(57) Abstract :
Embodiments of the present disclosure relates to a protective headgear for a visually impaired user to enhance safety and mobility for the visually impaired user. In an aspect, the headgear (102) includes a plurality of sensors and a processor (202) coupled to the plurality of sensors. The headgear (102) also includes a memory (204) coupled to the processor (202). The memory includes a set of instructions for the processor (202) to execute a sequence of tasks. The processor (202) is configured to capture images of environment and emit sound waves and combine the images of the environment and a reflection of the emitted sound waves to detect one or more objects. The processor (202) is further configured to identify the detected one or more objects and transmit information pertaining to the identified one or more objects to a user in real-time.

No. of Pages : 22 No. of Claims : 10