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## (54) Title of the invention: SYSTEM USING POLYNOMIALS ON FIELD PROGRAMMABLE GATE ARRAY (FPGA)

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### (57) Abstract:

ABSTRACT SYSTEM USING POLYNOMIALS ON FIELD PROGRAMMABLE GATE ARRAY (FPGA) The present disclosure discloses a system (102) using polynomials on a Field Programmable Gate Array (FPGA), comprising a polynomial-based encryption module (104) for transforming plain text into cipher text using key polynomials, a polynomial-based decryption module (106) using the Newton-Raphson to decrypt cipher text and recover the original plain text and a coding and implementation module (108) for performing floating-point operations, including addition, subtraction, multiplication, and division of mantissa and exponential formats. FIG. 1

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