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(57) Abstract :
 The present disclosure relates to a system 102 and method 400 for method 400 for implementing a Wireless Fidelity (Wi-Fi) module using a Field-Programmable Gate Array (FPGA). The method 400 includes compiling and executing 402 a design using a computer-aided environment, where the design is executed on a hardware platform comprising the FPGA and creating 404 a block design using a configuration of Intellectual Property (IP) cores, a configuration of a clock wizard, a configuration of a Wi-Fi, and a configuration of a soft processor within the computer-aided environment. Further, the method 400 includes providing 406 a system differential clock as an input to the block design for implementing the Wi-Fi module with the FPGA for wireless communication.

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