(12) PATENT APPLICATION PUBLICATION

(19) INDIA

(22) Date of filing of Application :24/08/2023

(54) Title of the invention : A SYSTEM AND METHOD FOR MANUFACTURING A FLEXIBLE ELECTRONICS COMPONENT

		(71)Name of Applicant : 1)Chitkara University
(51) Internationalclassification(86) International	:H01L0051000000, H05K0001090000, H05K0001020000, H01L0023498000, H01L0021316000	Address of Applicant :Chitkara University, Chandigarh-Patiala National Highway, Village Jhansla, Rajpura, Punjab - 140401, India Patiala
		2)Bluest Mettle Solutions Private Limited
		Name of Applicant : NA
Application No	:NA	Address of Applicant : NA
Filing Date (87) International	:NA	(/2)Name of Inventor:
	: NA	Address of Applicant :ODC-4, Panchshil Tech Park, inside
(61) Patent of Addition		Courtyard by Marriott premises, Hinjewadi Phase - 1, Pune -
to Application Number	:NA	411057, Maharashtra, India. Pune
Filing Date	:NA	2)PANDEY, Sakshi
(62) Divisional to		Address of Applicant :ODC-4, Panchshil Tech Park, inside
Application Number	:NA	Courtyard by Marriott premises, Hinjewadi Phase - 1, Pune -
Filing Date	:NA	411057, Maharashtra, India. Pune
		Address of Applicant :Chitkara University. Chandigarh-Patiala
		National Highway, Village Jhansla, Rajpura, Punjab - 140401,
		India. Patiala

(57) Abstract :

Embodiments of the present disclosure relates to a method (100) of manufacturing flexible electronics components and the development and production of electronic devices that possess flexibility, conformability, and versatility in terms of shape, allowing them to adapt to various surfaces and applications. The method begins with preparing a flexible substrate. Next, the method deposits active electronic components onto the flexible substrate. Next, the method deposits passive electronic components onto the flexible substrate. Next, the method deposits passive electronic components onto the flexible substrate. Next, the method deposits passive electronic components onto the flexible substrate. Next, the method creates conductive paths between the active and passive components. Next, the method forms vias to establish vertical interconnects between different layers of the flexible electronics component. Next, the method deposits dielectric layers. Next, the method applies a protective encapsulation layer. Next, the method utilizes flexible encapsulation materials. Next, the method conducts electrical testing and mechanical testing to evaluate the flexibility, durability, and resistance of the flexible electronics component.

No. of Pages : 19 No. of Claims : 10