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(57) Abstract :

The present subject matter relates to a low-pass filter circuit comprising a flipped voltage follower; a diode-connected current source; a first MOS transistor (P1) configured to receive an input signal; a second MOS transistor (P2) configured to provide shunt feedback; a current sink diode connected to a third MOS transistor (N1) with a resistance of 1/g_{m1}; a current source replaced with a diode-connected fourth MOS transistor (P3) with a resistance of 1/g_{m3}; and a first capacitor (C1) and a second capacitor (C2).

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