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(57) Abstract :

The present invention discloses a system (100) for decrypting AES messages on fly. The system includes a processor (102) that received data from one or more computing devices (112), separate the received data into blocks for Electronic Codebook (ECB) encryption, and generate round keys on the fly without explicitly using a specific array. The system also performs AES decryption using ECB and Counter (CTR) modes, while conserving memory by computing the round keys in each cycle. Furthermore, the system obtains a secret key and an initialization vector, allowing for the generation of a decrypted output. The disclosed system offers an efficient and secure approach to decrypting AES messages in real time, making it suitable for various applications requiring on-the-fly decryption capabilities.

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