

(12) PATENT APPLICATION PUBLICATION

(21) Application No.202211066817 A

(19) INDIA

(22) Date of filing of Application :21/11/2022

(43) Publication Date : 09/06/2023

(54) Title of the invention : SAFEGUARDING A DIGITAL CIRCUIT AGAINST GLITCH

(51) International classification :H03K0019018500, H01R0012730000, G06F0021550000, H04L0069329000, H03K0007080000

(86) International Application No :NA
Filing Date :NA

(87) International Publication No : NA

(61) Patent of Addition to Application Number :NA
Filing Date :NA

(62) Divisional to Application Number :NA
Filing Date :NA

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(57) Abstract :

The present disclosure discloses a system 100 and a method 400 to detect a glitch present in a digital circuit includes a pulse-width modulator 102 in the digital circuit, a switch circuit 104 to connect the circuit to a DC power supply 106, and a detector circuit 108. The digital circuit is configured to execute one or more functions including detecting a positive as well as a negative peak in the internal voltage and resetting the system 100 and preventing damage to the digital circuit after comparing the faulty output with the standard output.

No. of Pages : 15 No. of Claims : 10