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(51) International classification	:H01L0029786000, H01L0027120000, H03K0019018500, G11C0019280000, H04R0003000000	(71)Name of Applicant : <b>1)Chitkara Innovation Incubator Foundation</b> Address of Applicant :SCO: 160-161, Sector - 9c, Madhya Marg, Chandigarh- 160009, India. Chandigarh India
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(57) Abstract :

The present disclosure pertains to a composite partial positive feedback based system (100) , the system (100) including a first branch (102) with a first composite transistor (102-1) along with a set of transistor devices (102-1-1, 102-1-2), a second composite transistor (102-2) with a set of transistor devices (102-2-1, 102-2-2) , a second branch (104) including a third composite transistor (104-1) with a set of transistor devices (104-1-1, 104-1-2), a fourth composite transistor (104-2) with a set of transistor devices (104-2-1, 104-2-2) , where drain voltage of the first transistor device (102-2-1) of the second composite transistor (102-2) is fed to a gate of the first transistor device (104-1-1) of the third composite transistor (104-1), and drain voltage of the first transistor device (104-1-1) of the third composite transistor (104-1) is fed to a gate of the first transistor device (102-2-1) of the second composite transistor (102-2) to effect a positive feedback configuration in the second composite transistor (102-2) and the third composite transistor (104-1) thereby boosting corresponding values of transconductance.

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