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(57) Abstract :

The present disclosure pertains to a digital learning system (200) including a display unit (102) configured to display augmented reality (AR) simulation of predefined matrix, where the predefined matrix is configured to accommodate a set of binary digits, and correspondingly generate a first set of signals, an augmented reality (AR) engine (202) configured to extract an equation associated with the set of binary digits, authenticate the equation with preregistered equation, display a digital circuit based on the verified equation and illustrate prototype of one or more components associated with the digital circuit. The system (200) includes a board configured to assemble the one or more components (214) and pertain to an actual circuit, a processing unit (208) configured to verify output of the displayed digital circuit with output of the actual circuit and generate a set of alarm signals upon negative verification. The system (200) includes an image acquisition unit 210 and a set of sensors (212).

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