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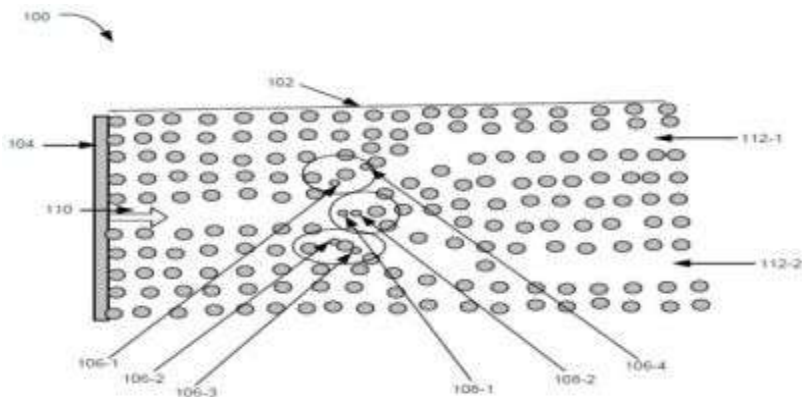
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(57) Abstract :

The present disclosure provides an opto-electronic integrated circuit. The integrated circuit can include a power splitter having an input port and one or more output ports; an input source configured to generate a first set of optical signals to the power splitter via the input port; a first set of defect rods introduced on bends, located on both sides of the power splitter, to reduce backward reflections; and a second set of defect rods placed at a junction of the power splitter, the second set of defect rods can be configured to suppress bending losses, resonate the first set of optical signals across the one or more output ports and transfer maximum power to the one or more output ports.



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