(19) INDIA

(22) Date of filing of Application :26/06/2019

(43) Publication Date: 01/01/2021

(54) Title of the invention : AUGMENTED REALITY BASED LEARNING SYSTEM AND METHOD FOR ELECTRONIC CIRCUITS

(51) International classification	:A61B0005000000, G06Q0010100000, G06F0001260000, G06F0017500000, G08B0021180000	(71)Name of Applicant: 1)Chitkara Innovation Incubator Foundation Address of Applicant: SCO: 160-161, Sector -9c, Madhya Marg, Chandigarh- 160009, India. Punjab India (72)Name of Inventor:
(31) Priority Document No	:NA	1)SHARMA, Bhanu
(32) Priority Date	:NA	2)MANTRI, Archana
(33) Name of priority country	:NA	
(86) International Application No	:NA	
Filing Date	:NA	
(87) International Publication No	: NA	
(61) Patent of Addition to ApplicationNumberFiling Date	:NA :NA	
(62) Divisional to Application Number	:NA	
Filing Date	:NA	

(57) Abstract:

The present disclosure provides AR-based learning system 100 and method 200 for electronic circuits. The system 100 includes: first computing device 106 having one or more processors 102 and memory 104; and scanning unit 108 to scan AR markers placed by users on predefined location and generate scanned images. The AR markers include data of electronic circuits. The first computing device 106 is configured to display the electronic circuits on display unit 110 to allow users to implement the displayed electronic circuits on hardware circuit kit 112. The hardware circuit kit 112 generates output data based on the implementation. The first computing device 106 compares the generated output data with predefined output data that is stored in a database 114, and generates first data as either correct or incorrect based on the comparison of the generated output data and the predefined output data.



No. of Pages: 30 No. of Claims: 6