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(57) Abstract :

An improved quantum-dot cellular automata (QCA) cell-based gate design for an efficient RAM cell is disclosed. According to an embodiment, QCA cell-based gate design can include: a plurality of QCA cells to provide a route to communicate data; at least one output QCA cell configured with the plurality of QCA cells; and at least five input QCA cells configured with the plurality of QCA cells, wherein arrangement of the at least one output QCA cell and the at least five input QCA cells with the plurality of QCA cell allow single as well as multilayer design of the RAM cell, and wherein the arrangement of the at least one output QCA cell, the at least five input QCA cells with the plurality of QCA cells is adapted to reduce input to output delay of the RAM cell.

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